



Approved for use through 09/30/00. OMB 0651-0032
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
or the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Form to be used for Regular Continuation/Divisional DO NOT LIGHT FOR THE CONTROL OF THE PROPERTY. Form to be used for Regular Continuation/Divisional. DO NOT USE FOR FWC

UTILITY	7	Attorney Docket	No.	143-3000-1	CUS		
	T APPLICATION First Named In Application Ide			er			
TRANSMITTAL Title		Title		A Dual-Direction Over-Voltage And Over-Current IC Protection Device And Its Cell Structure			
(Only for new nonprovisional applications under 37 CFR 1.53(b)) Express Mail Lab			el No.	No. EL 901563753			
APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents.			ADDRESS TO: Commissioner for Patents Box Patent Application Washington, D.C. 20231				
1. See Transmittal Form - see page 2 of this form. (Submit an original, and a duplicate for fee processing) 2. Application: Specification: (preferred arrangement set forth below) Descriptive title of the Invention, Cross References to Related Applications, Reference to Microfiche Appendix, Background of the Invention, Brief Summary of the Invention, Brief Description of the Drawings, and Detailed Description (all totaling 21 pages) Appendix(ces), &			6.				
Continuation Divisional of prior application No. 09/100,384 Filed on June 19, 1998, entitled: A Dual-Direction Over-Voltage And Over-Current IC Protection Device And Its Cell Structure							
PRIOR APPLICATION INFORMATION: Examiner S. Hu Group Art Unit 2811 18. CORRESPONDENCE ADDRESS							
Customer Number or Bar (Name Edwardtorneys for Applicant Address 25 M	Code Label ard C. Kwok ven Morrill M etro Drive, Suite			Reg. N	No. 33,938	espondence address belo)W
City San J Country: Unite		Stat (408) lephone	te (453-920		Zip Code	95110 (408) 453-7979	



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Deane, Peter; Tsay, Chen H.; Wang, Albert

Assignee:

National Semiconductor Corporation

Title:

A Dual-Direction Over-Voltage And Over-Current IC Protection

Device And Its Cell Structure

Serial No.:

09/100,384

Filing Date:

June 19, 1998

Examiner:

Not Known

Group Art Unit: Not Known

Docket No.:

NS-3868 US

San Jose, California October 23, 2001

Box Patent Application Attn: Official Draftsperson COMMISSIONER FOR PATENTS Washington, D. C. 20231

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit twelve (12) sheets of formal drawings, consisting of Figures 1A, 1B, 1C, 1D, 2, 3, 4, 5, 6, 7, 8A, 8B, 9A, 9B, 10A, 10B, 11A11B. 12, 13, 14), in the abovenamed application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

EXPRESS MAIL LABEL NO:

EL901563753US

Edward C. Kwok

Attorney for Applicants

Reg. No. 33,938

LAW OFFICES OF LIERVEN MORRILL MacPHERSON LLP

25 METRO DRIVE SUITE 700 SAN JOSE, CA 95110 (408) 453-9200 FAX (408) 453-7979

813809 v1